

FIG. 1 (Prior Art)

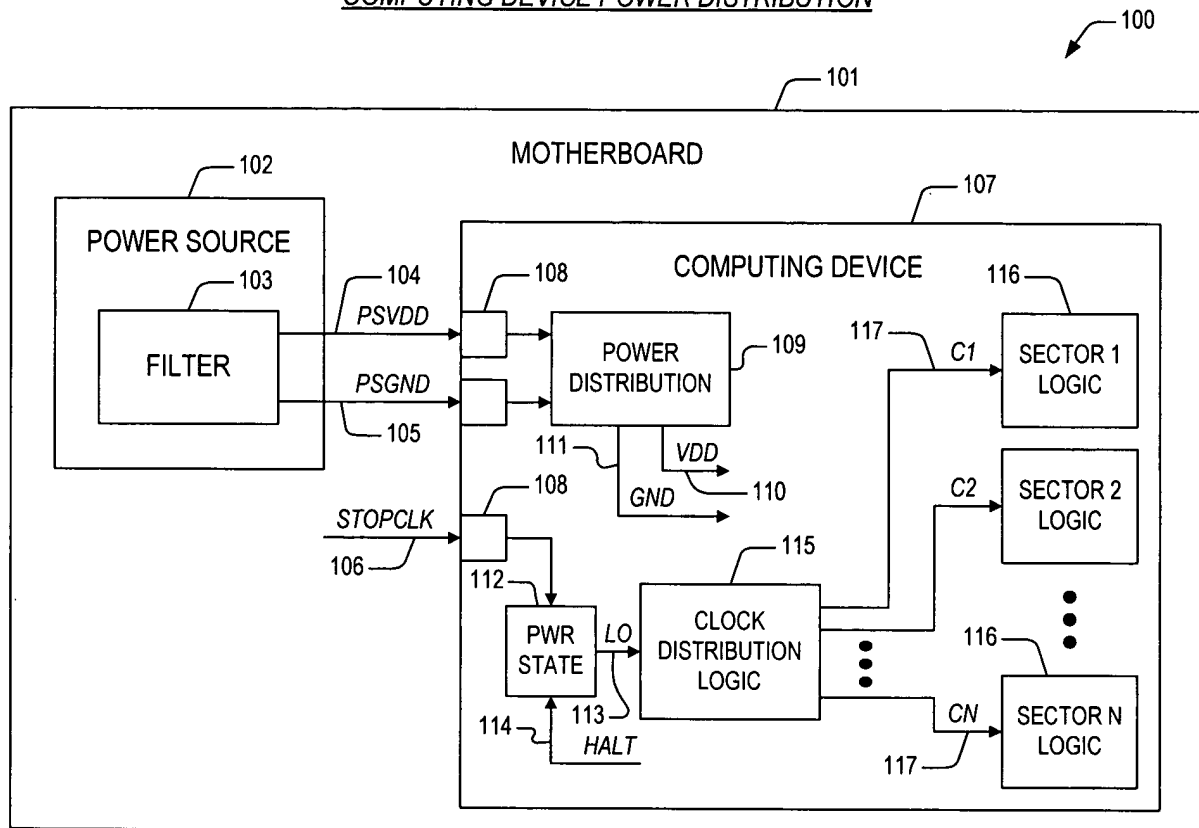
COMPUTING DEVICE POWER DISTRIBUTION



FIG. 2 (Prior Art)

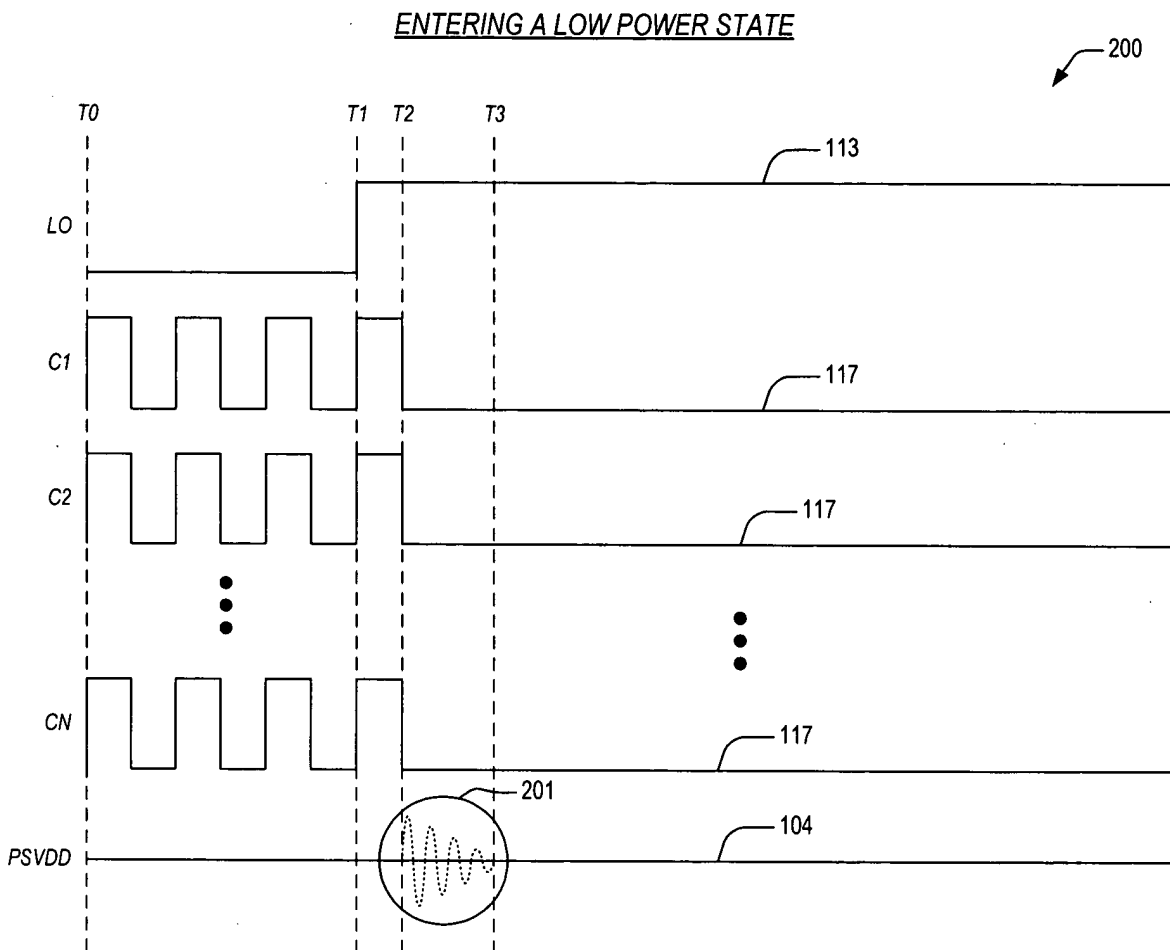


FIG. 3

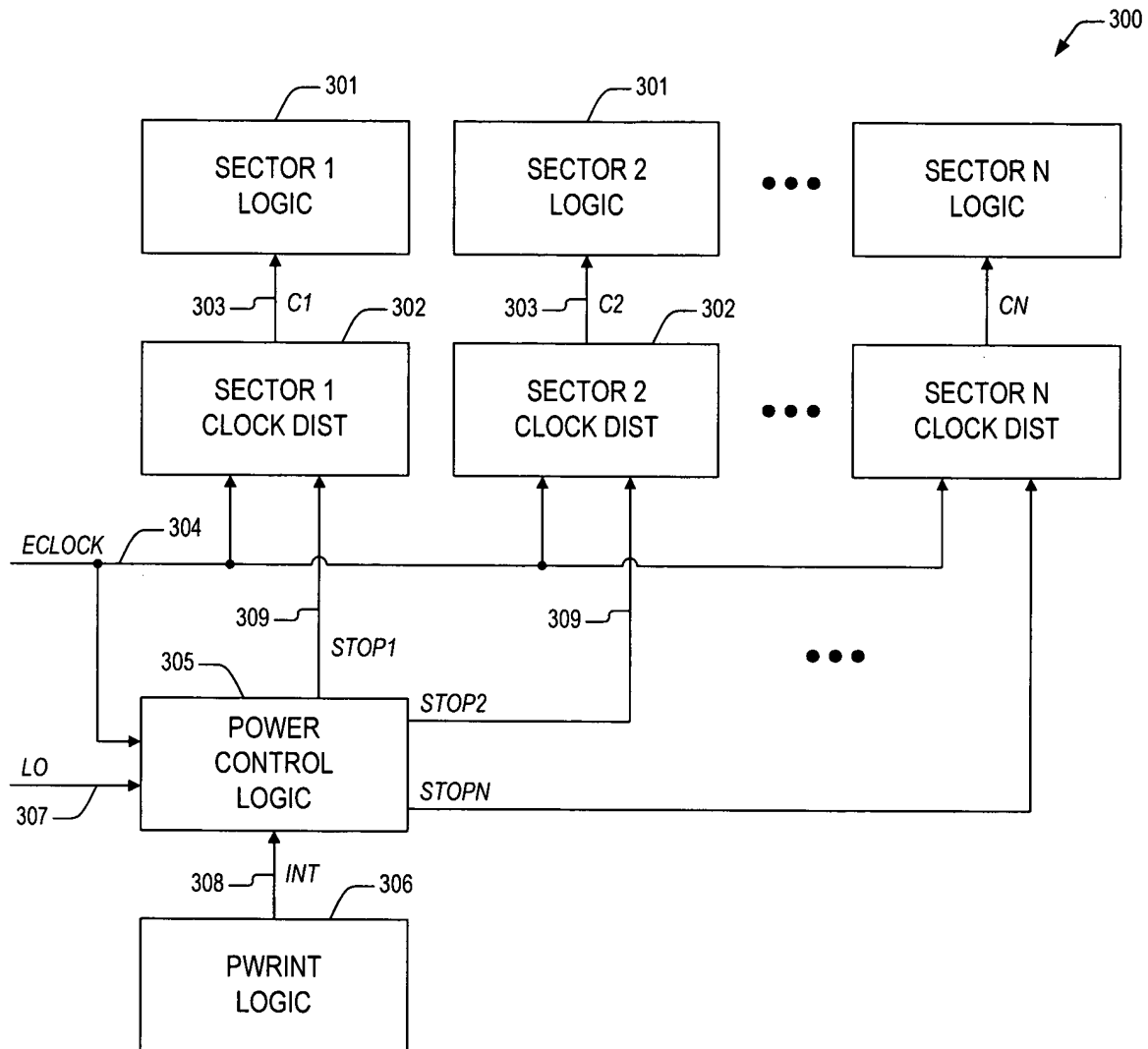
MECHANISM FOR MEASURED POWER MANAGEMENT TRANSITIONS



FIG. 4

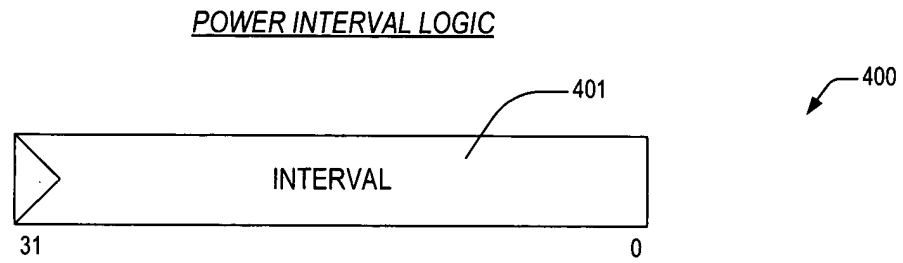


FIG. 5

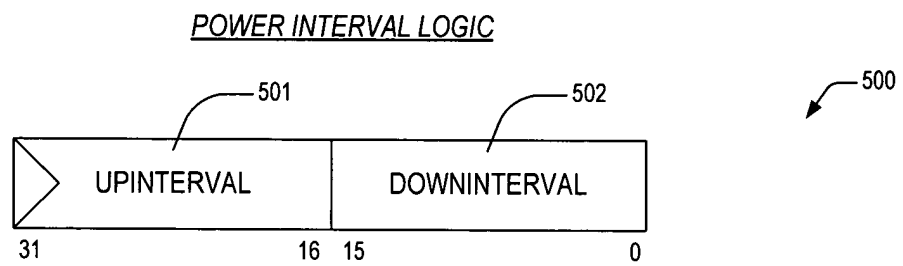


FIG. 6

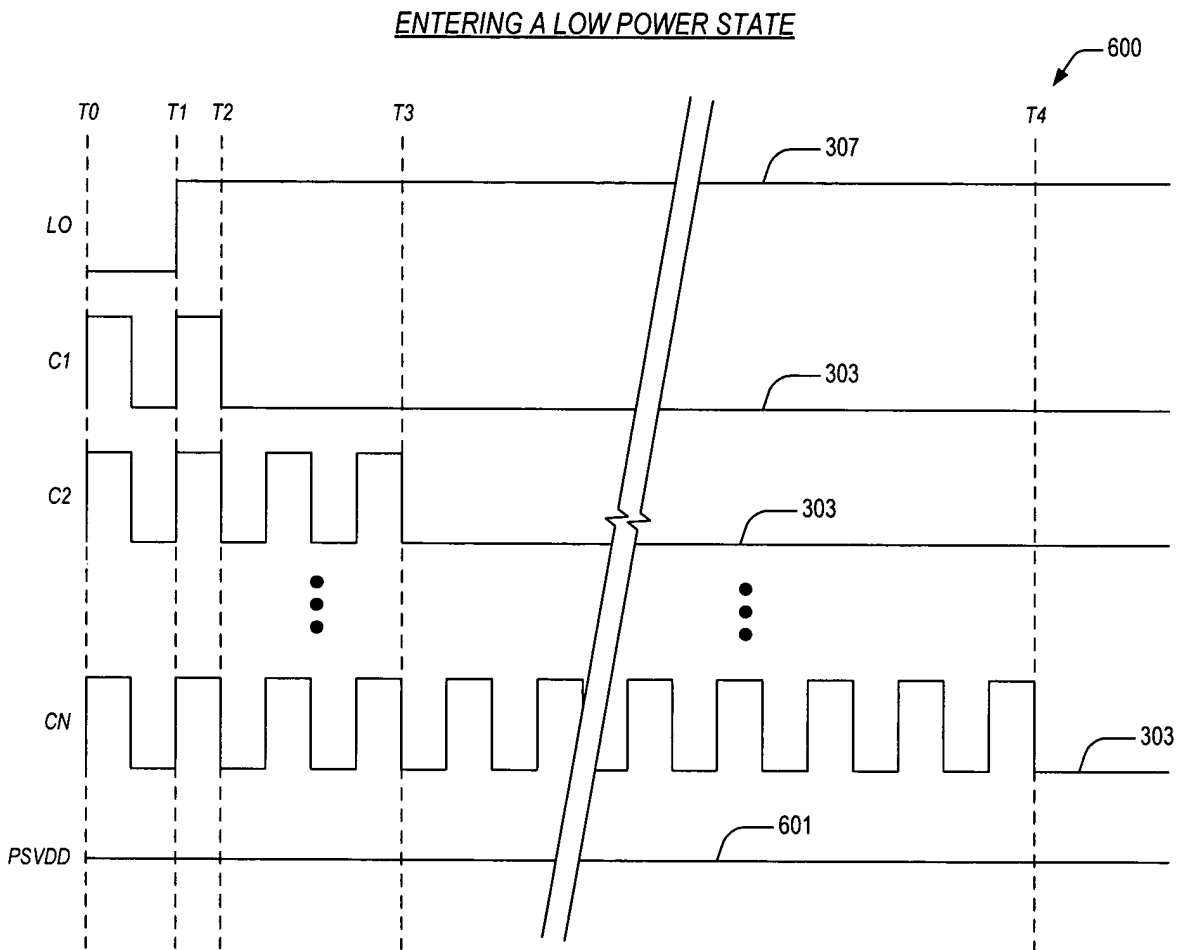


FIG. 7

METHOD FOR MEASURED POWER MANAGEMENT TRANSITIONS